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**INFORMATION DISCLOSURE STATEMENT
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Applicant: ELLIS

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Group Art Unit: 2153

Date: May 16, 2001

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Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
AR	5,678,028	10/1997	Bershteyn et al.			
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OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

QR	Sakano et al., "A Three-Dimensional Mesh Multiprocessor System Using Board-to-Board Free-Space Optical Interconnects: COSINE-III," IEEE, 1993, pages 278-283, XP 000463415			
RR	Nowatzky et al., "Are Crossbars Really Dead? The Case for Optical Multiprocessor Interconnect Systems," Proceedings of the Annual Symposium on Computer Architecture, ACM, volume 22, June 1995, pages 106-115, XP 000687800			
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Examiner

D. DINH

Date Considered:

11/20/01

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.